

## **REGISTRATION FORM**

**Two Day National Workshop  
On  
Model Based Design for Power Electronics &  
Embedded Systems**

**December 12<sup>th</sup> & 13<sup>th</sup> 2013**

1. Name :
2. Designation :
3. Organization :
4. Mailing Address :

Ph.No. :  
E.mail(compulsory) :

5. Participation category :  
(Faculty / Industry / R&D / PG Student)

6. Registration Fee Details :

Bank Name.....Date: .....

D.D. No.: ..... Amount: .....

Date: (Signature of Applicant)

Recommendation of Head of the Institute /  
organization:

I recommend his/her participation in the course

Signature  
(Head of the Institute/organization with seal)

Station : Date :

## **ORGANISING COMMITTEE**

### **Patrons**

**Dr. K. Basavapunniah**

President, RVR &JC College of Engineering

**Sri R. Gopala Krishna**

Secretary & Correspondent, RVR & JC CE

**Dr. M. Gopalkrishna**

Treasurer, RVR &JC College of Engineering

### **Chairman**

**Dr. A.Sudhakar**

Principal, RVR &JC College of Engineering

### **Convener**

**Dr. K. Chandra Sekhar**

Professor & Head, Dept. Of EEE

### **Coordination Committee**

Smt K. Swarna Sri, Associate Professor

Smt K. Radha Rani, Associate Professor

Sri G.B. Sankara Rao, Associate Professor

Sri N. C. Kotaiah, Associate Professor

**Filled in Application form with Registration fee**

**DD is to be sent to:**

Convener,

Department of EEE,

R.V.R. & J.C. College of Engineering,

Chandramoulipuram, Chowdavaram,

GUNTUR – 522 019, A.P., INDIA

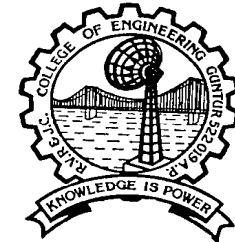
Phone: 9393194449, 94901073317, Ext(170)

Email: cskoritala@rediffmail.com

**Two Day National Workshop  
On  
Model Based Design for Power Electronics  
& Embedded Systems**

**December 12<sup>th</sup> & 13<sup>th</sup> 2013**

**Organised by  
Department of  
Electrical & Electronics Engineering**



**R. V. R. & J. C.  
COLLEGE OF ENGINEERING**

(Autonomous)

(Approved by AICTE :: Accredited by NBA)

(Sponsored by Nagarjuna Education Society)

Chowdavaram, Chandramoulipuram,

**GUNTUR – 522019 A.P., INDIA**

Phone: 94901073317, 18

www.rvrjce.ac.in

## About The College

The Rayapati Venkata Ranga Rao & Jagarlamudi Chandramouli (RVR & JC) College of Engineering was promoted by the renowned Nagarjuna Education Society (1967) in the year 1985, the College drew its initial impetus from Peoples Representatives, local doctors, charitable trusts and commercial houses of Guntur District. The promoters of the College have pursued their mission with such a zeal and understanding that the College had over taken the older colleges quantitatively and qualitatively. The premier status of the institution is made possible by sticking to our core-principles of student-focus, Human Resource Development and emphasis on total quality. Training the students beyond the defined borders of the curriculum is a basic emphasis, the college dearly practices. The college periodically tested its own standards and standing among the fraternity of colleges, securing Accreditation from National Board of Accreditation, AICTE in, 2002, 2007 & 2012. Today it is a most sought after institution offering 7 B.Tech. Degree Courses, 6 M.Tech courses in Engineering, MCA & MBA. The institute was conferred Autonomous status by UGC, New Delhi from the Academic Year 2012-13.

## About the Department

The department of Electrical & Electronics Engineering came into existence in 1994. It was started with an intake of 60 students which was subsequently raised to 180 over a period of time. It has a strong administration, infrastructure and dedicated team of Faculty. The department is keeping pace with the fast changing technology of Electrical & Electronics Engineering in terms of its teaching program and laboratory activities. In addition to core electrical subjects, software based subjects are also included in the syllabus which makes this course suitable for core Electrical & Electronics Engineering and many IT industries. Department was accredited

Three times by National Board of Accreditation (NBA) in the years 2002, 2007 and 2012 with good grades. The department is presently offering 4-year B.Tech degree programme in Electrical & Electronics Engineering and 2-year M.Tech degree programme in Power Systems Engineering.

## Course Objective

The main objective of the workshop is to provide an opportunity to the faculty members of engineering colleges and practicing engineers to model and simulate dynamic systems of controller and plant, as well as analyze and visualize data, and generate efficient embedded target programs using VisSim. VisSim provides an excellent platform for introducing fundamental concepts in dynamic simulation and control theory, and practical experience in model-based design and development.

Attend this hands-on workshop to see how you can speed the embedded development process using modern model based development tools. The sessions are designed for both new and advanced VisSim users. Using hands-on lessons, the resource persons will demonstrate best practices and techniques for model construction, simulation, code generation, and embedded development for a Texas Instruments target. Attendees will receive a working TI kit with a 32 bit Piccolo processor, ADC, PWM, UART, I2C, GPIO etc, plus USB linkage to PC. The kit will be used for instruction during the course.

## Faculty

Resource persons are drawn from Visual Solutions, Inc. & Industry.

## Eligibility

The workshop is open to the faculty members of AICTE approved technical institutions, Research Scholars, persons working in R&D Organizations & Industries, Electrical, Electronics, Mechanical, and Mechatronics Engineers, Control and Process

Engineers, DSP and Embedded Software Engineers. The number of participants is limited to forty. The completed application should reach the Convener on or before **5<sup>th</sup> Dec, 2013**. The selected participants will be informed by **7<sup>th</sup> Dec, 2013**.

## Registration and Fee Particulars

Registration for participating in the workshop may be done by sending the application in the prescribed format duly signed by the Head of the Institution. Application form may be downloaded from the college website: [www.rvrjce.ac.in](http://www.rvrjce.ac.in)

## Fee Particulars

Teachers, Research scholars	
& PG students	Rs.500/-
Engineers from Industry	Rs.1000/-

Registration fee should be paid in the form of Demand Draft drawn on any Nationalized bank in favor of '**R.V.R. & J.C. College of Engineering**', payable at Andhra Bank(1483), Chowdavaram OR payable at Guntur.

## Accommodation

Participants are requested to make their own arrangements. No TA/DA will be paid to the participants.

## Important Dates

Last date for submission of application : 05.12.2013  
Intimation of selected candidates : 07.12.2013  
Registered participants are provided with participation certificate and Boarding during workshop days.

**Note:** As the workshop is based on hands on sessions, number of participants is restricted to 40 (maximum). Selection will be on first come first serve basis.